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1C903 U.S. PTO
09/891310

		Subclass
	Class	ISSUE CLASSIFICATION

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

O.I.P.E. **PATENT DATE**
n/a 1/1/00
SCANNED O.A. **WF**

APPLICATION NO. 09/891310	CONT/PRIOR F	CLASS 327 714	SUBCLASS 727	ART UNIT 2816	EXAMINER BRITT J. S. RODGERS
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Masahito Suzuki RSD

Integrated circuit device having boundary scan register

TIME

PTO-2040
12/99

ISSUING CLASSIFICATION

Continued on Issue Slip Inside File Jacket

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____				ISSUE FEE	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
				(Legal Instruments Examiner) (Date)	

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